

Substitute for Form 1449 A & B/PTO  <b><u>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</u></b>  <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number	Not Yet Assigned	
			Confirmation Number	Not Yet Assigned	
			Filing Date	December 19, 2005	
			First Named Inventor	Kenichi SHIMURA	
			Art Unit	Not Yet Assigned	
			Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	Q92193

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code <sup>2</sup> (if known)		
		US 2003/0111626	A1	06-19-2003	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation <sup>4</sup>
		Country Code <sup>2</sup>	Number <sup>3</sup>	Kind Code <sup>2</sup> (if known)			
		JP	2002-289807	A	10-11-2002		
		JP	2002-280526	A	09-27-2002		
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>4</sup>
		Durlam M. et al. "A LOW POWER 1 MBIT MRAM BASED ON 1T1MTJ BIT CELL INTEGRATED WITH COPPER INTERCONNECTS," 2002 Symposium on VLSI Circuits Digest of Technical Papers, pages 158 to 161.	

Examiner Signature	/Alexander Sofocleous/	Date Considered	03/08/2008
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /A.S./